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APPLICATION NUMBER	FILING or 371(c) DATE	GRP ART UNIT	FIL FEE REC'D	ATTY. DOCKET NO	TOT CLAIMS	IND CLAIMS
10/533,550	11/17/2005	2818	1500	I432.116.101/P29858	22	3

CONFIRMATION NO. 6310

CORRECTED FILING RECEIPT



25281
DICKE, BILLIG & CZAJA
FIFTH STREET TOWERS
100 SOUTH FIFTH STREET, SUITE 2250
MINNEAPOLIS, MN 55402

Date Mailed: 06/06/2008

Receipt is acknowledged of this non-provisional patent application. The application will be taken up for examination in due course. Applicant will be notified as to the results of the examination. Any correspondence concerning the application must include the following identification information: the U.S. APPLICATION NUMBER, FILING DATE, NAME OF APPLICANT, and TITLE OF INVENTION. Fees transmitted by check or draft are subject to collection. Please verify the accuracy of the data presented on this receipt. **If an error is noted on this Filing Receipt, please submit a written request for a Filing Receipt Correction. Please provide a copy of this Filing Receipt with the changes noted thereon. If you received a "Notice to File Missing Parts" for this application, please submit any corrections to this Filing Receipt with your reply to the Notice. When the USPTO processes the reply to the Notice, the USPTO will generate another Filing Receipt incorporating the requested corrections**

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Domestic Priority data as claimed by applicant

This application is a 371 of PCT/DE03/03587 10/29/2003

Foreign Applications

GERMANY 10250868.2 10/31/2002

If Required, Foreign Filing License Granted: 06/05/2008

The country code and number of your priority application, to be used for filing abroad under the Paris Convention, is **US 10/533,550**

Projected Publication Date: Not Applicable

Non-Publication Request: No

Early Publication Request: No

Title

Vertically integrated field-effect transistor and method for fabricating

Preliminary Class

257

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